FIG. 1A (PRIOR ART)

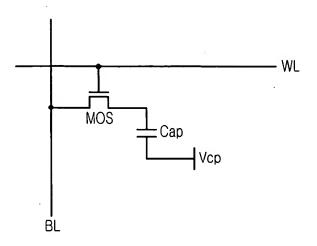


FIG. 1B (PRIOR ART)

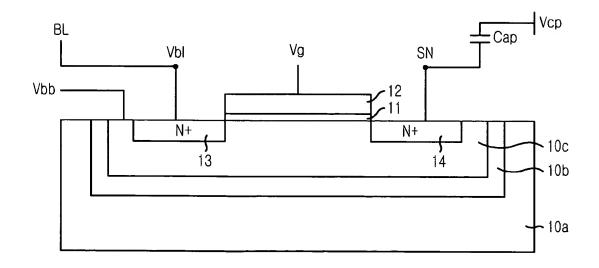


FIG. 2 (PRIOR ART)

€

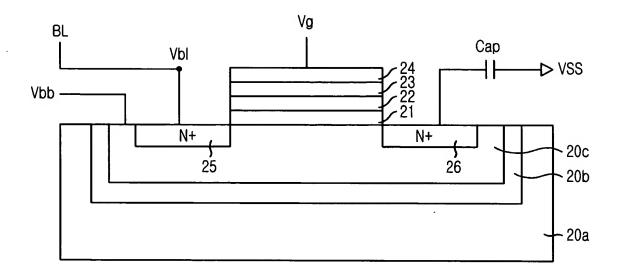


FIG. 3

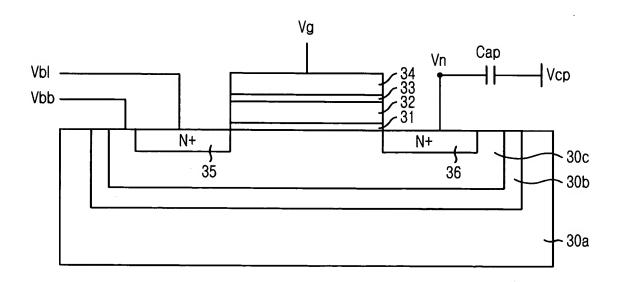


FIG. 4A

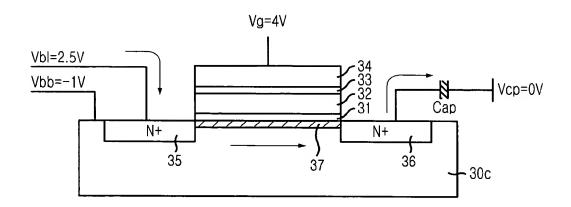
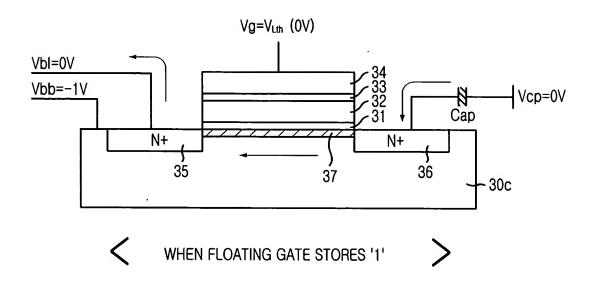


FIG. 4B



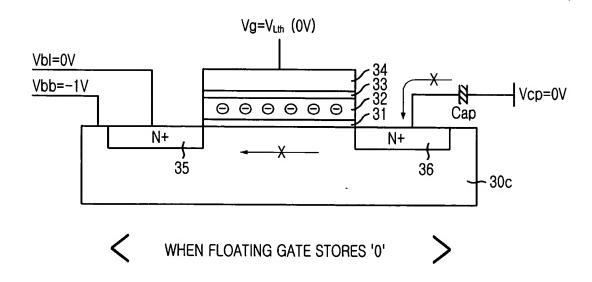
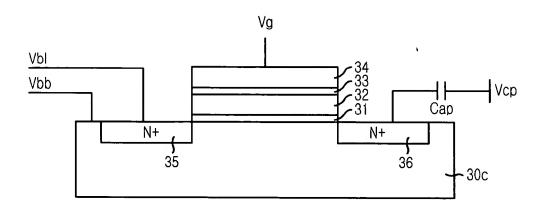


FIG. 4C



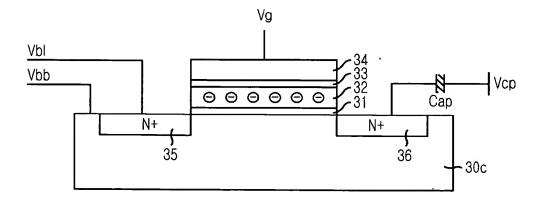
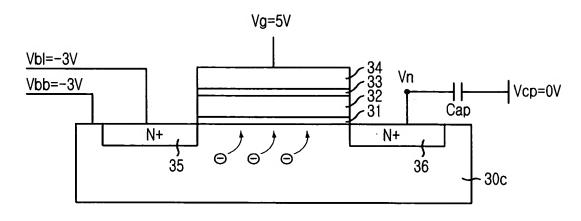
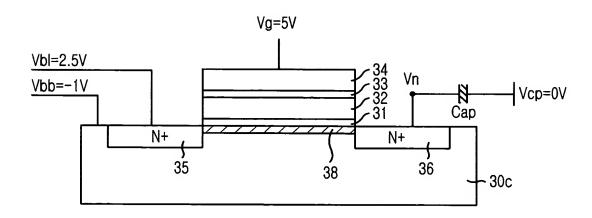


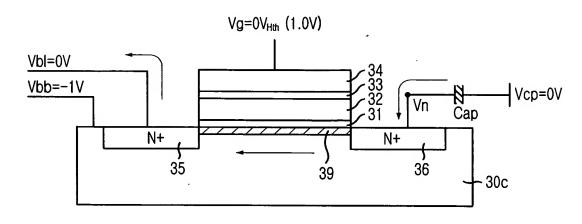
FIG. 5A



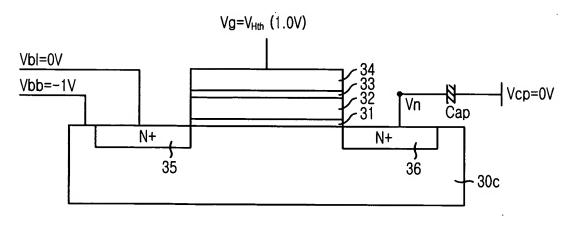
<WHEN THRESHOLD VOLTAGE IS HIGHER THAN V_{Hth} >

FIG. 5B



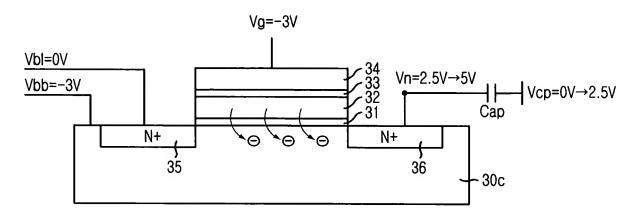


<WHEN THRESHOLD VOLTAGE IS V_{Hth} >

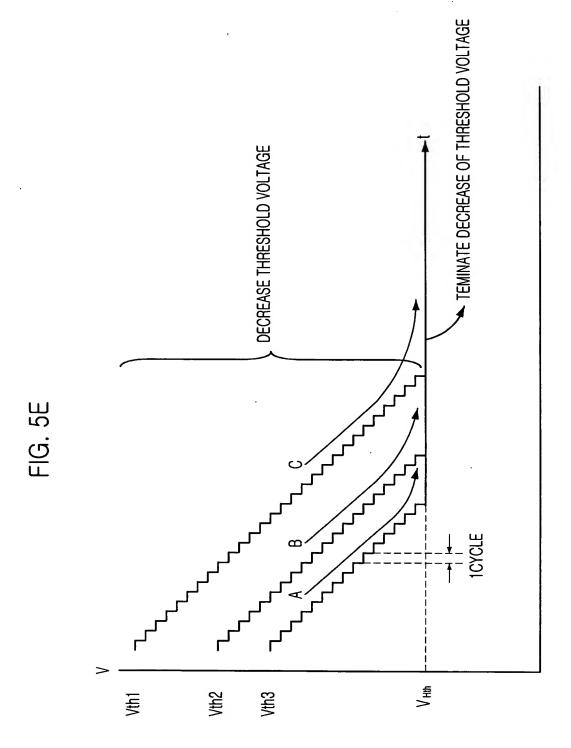


WHEN THRESHOLD VOLTAGE IS HIGHER THAN V $_{\rm Hth}>$

FIG. 5D



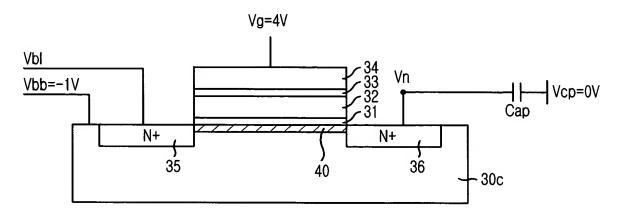
<THRESHOLD VOLTAGE IS DECREASED>



 ∂_{τ}^{σ}

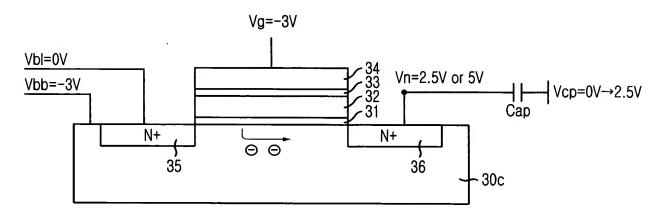
4.

FIG. 6



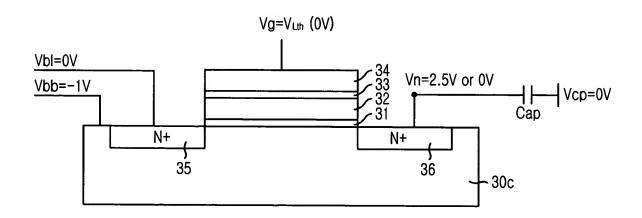
<WHEN THRESHOLD VOLTAGE IS HIGHER THAN V_{Hth} >

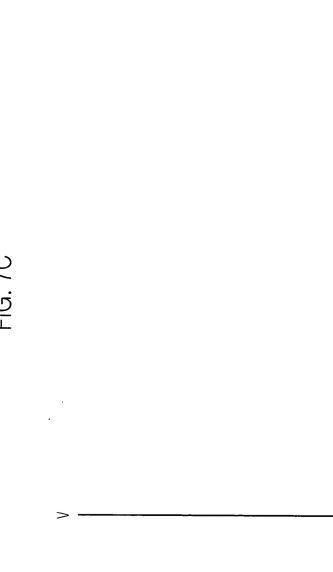
FIG. 7A



<THRESHOLD VOLTAGE IS DECREASED>

FIG. 7B







◆ WHEN CAPACITOR STORES '0'

Vth.h

WHEN CAPACITOR STORES '1'

FIG. 8

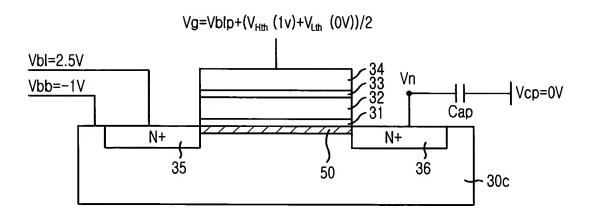
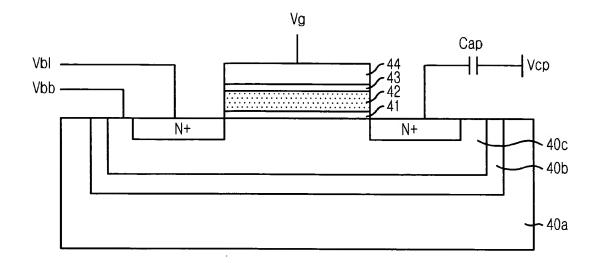


FIG. 9



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